

FIG.1

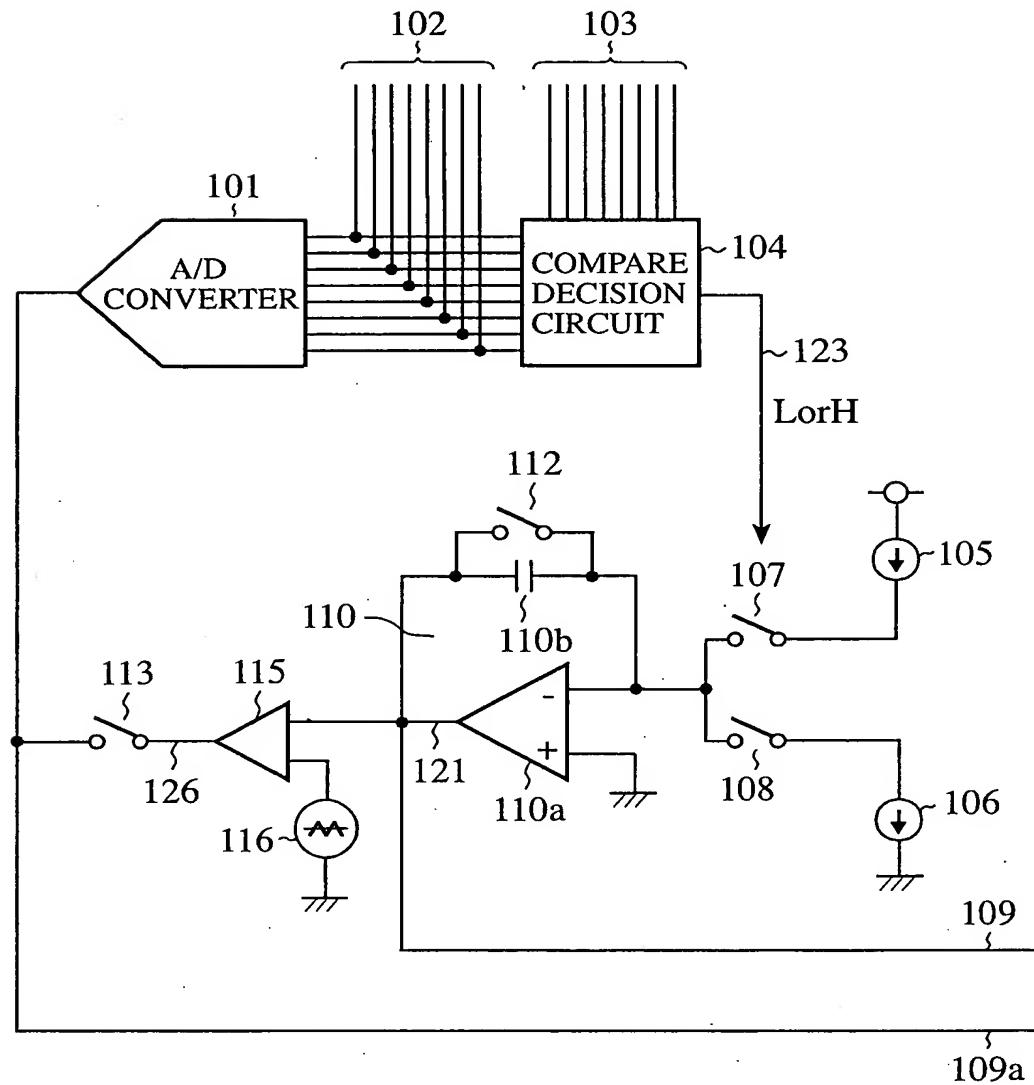


FIG.2

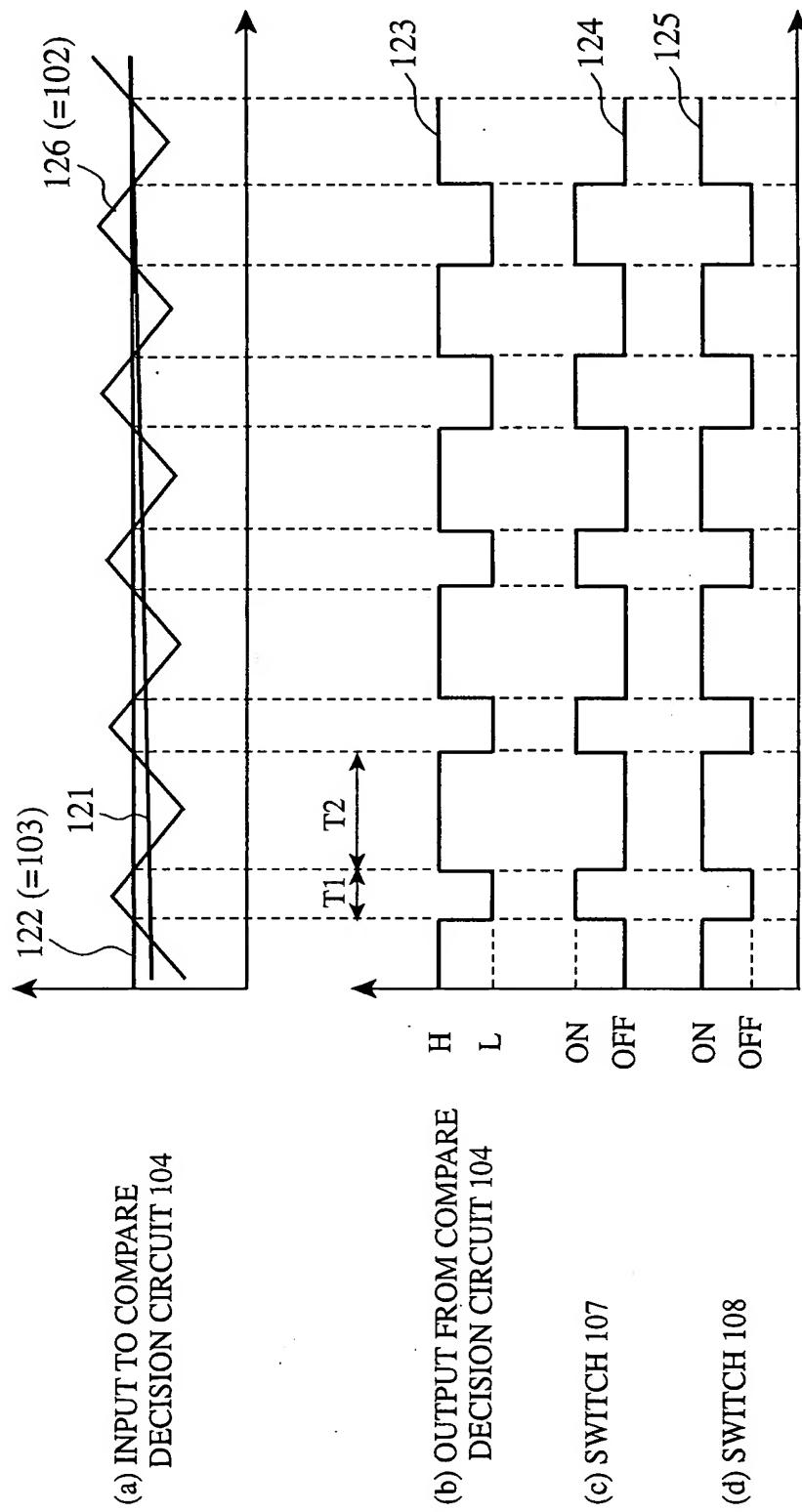


FIG.3

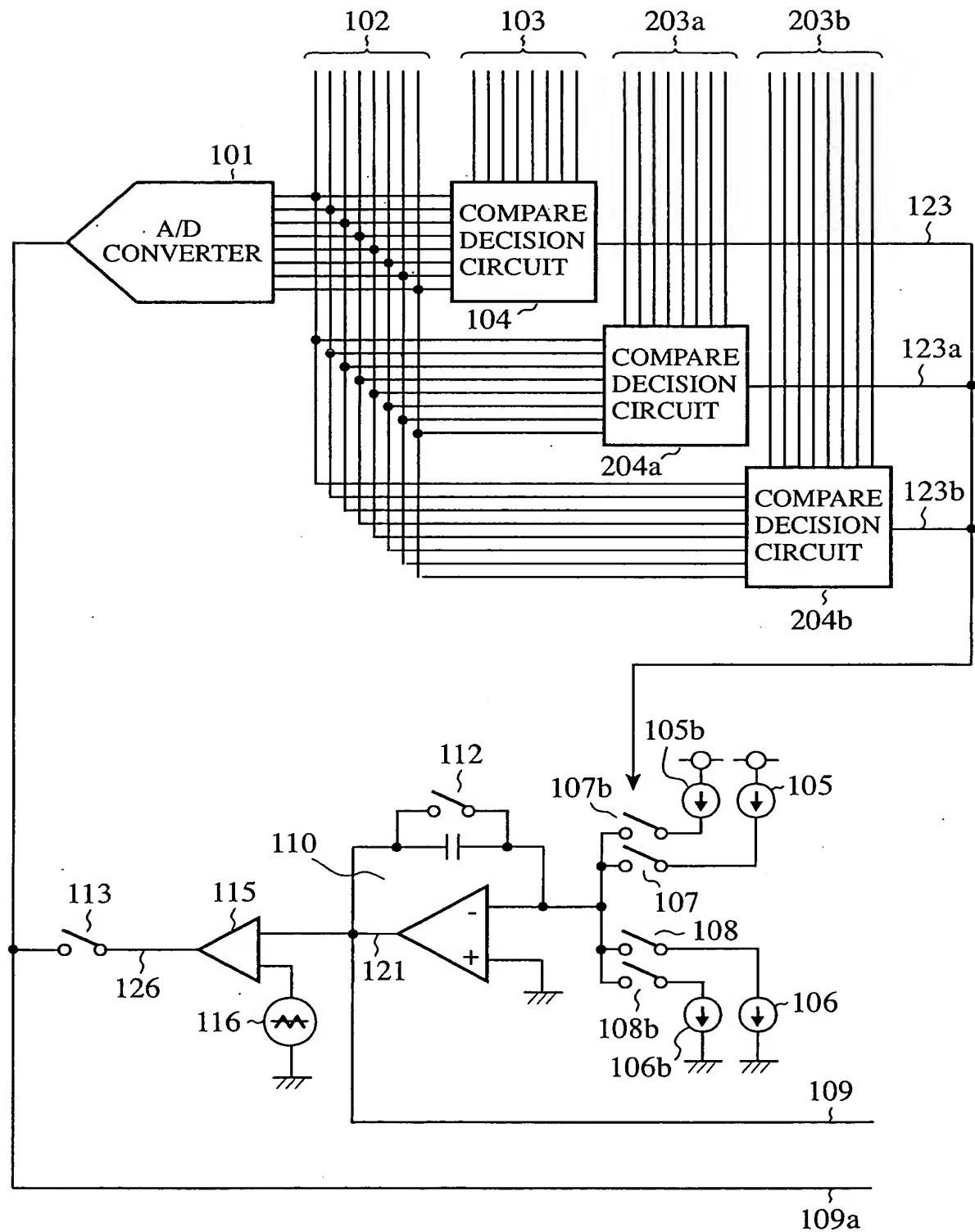
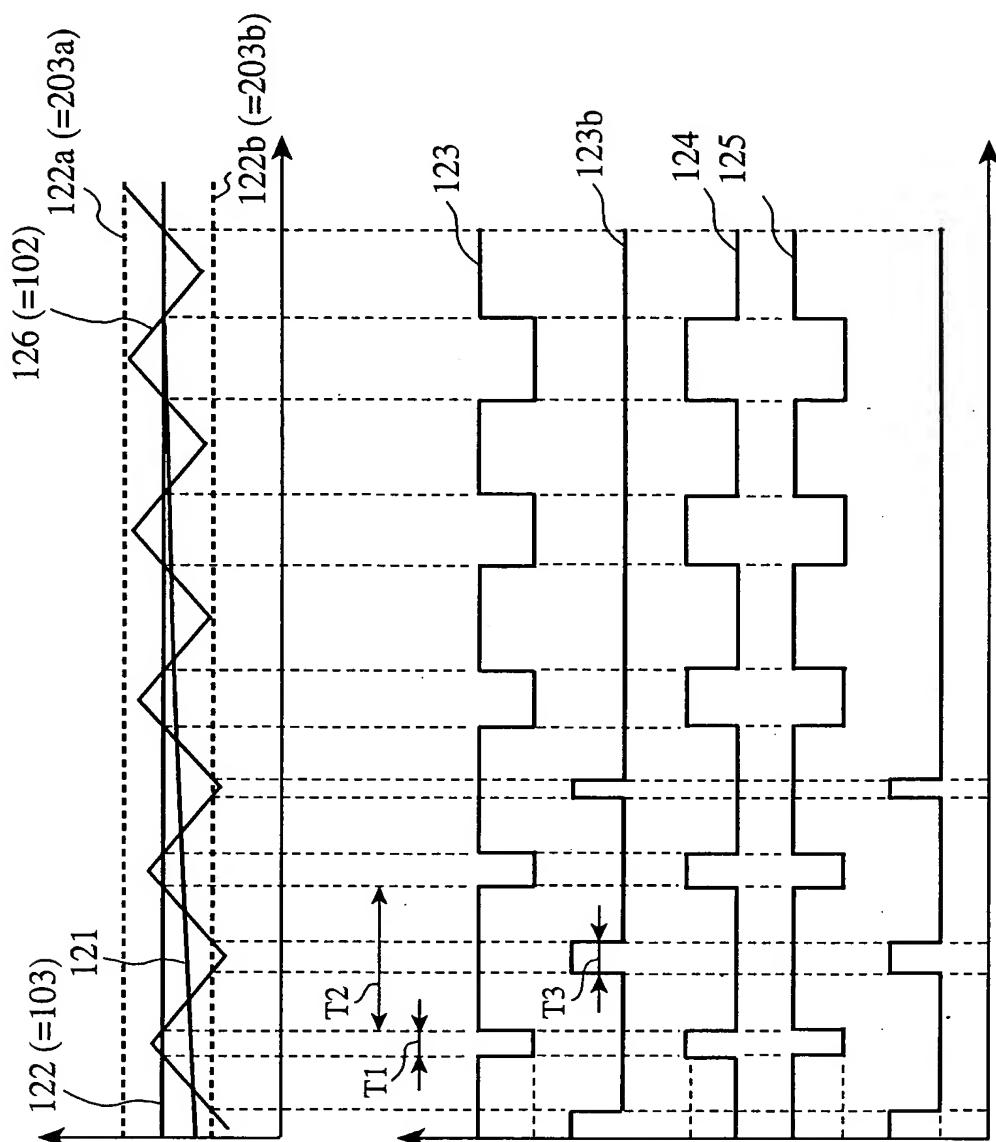


FIG.4



(a) INPUT TO COMPARE DECISION
CIRCUITS 104, 204a AND 204b

(b) OUTPUT FROM COMPARE
DECISION CIRCUIT 104

(c) OUTPUT FROM COMPARE
DECISION CIRCUIT 204b

(d) SWITCH 107

(e) SWITCH 108

(f) SWITCH 108b

FIG.5

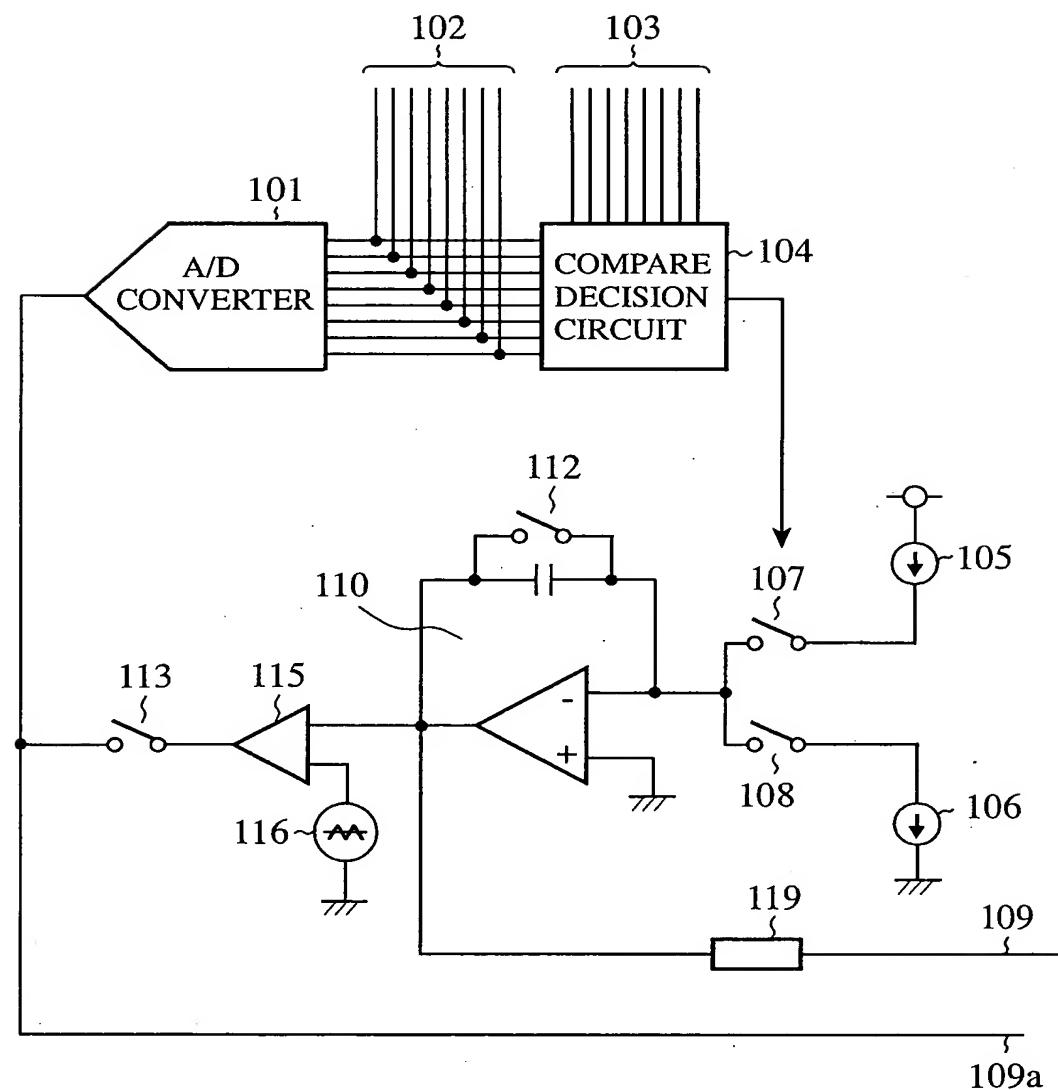


FIG.6

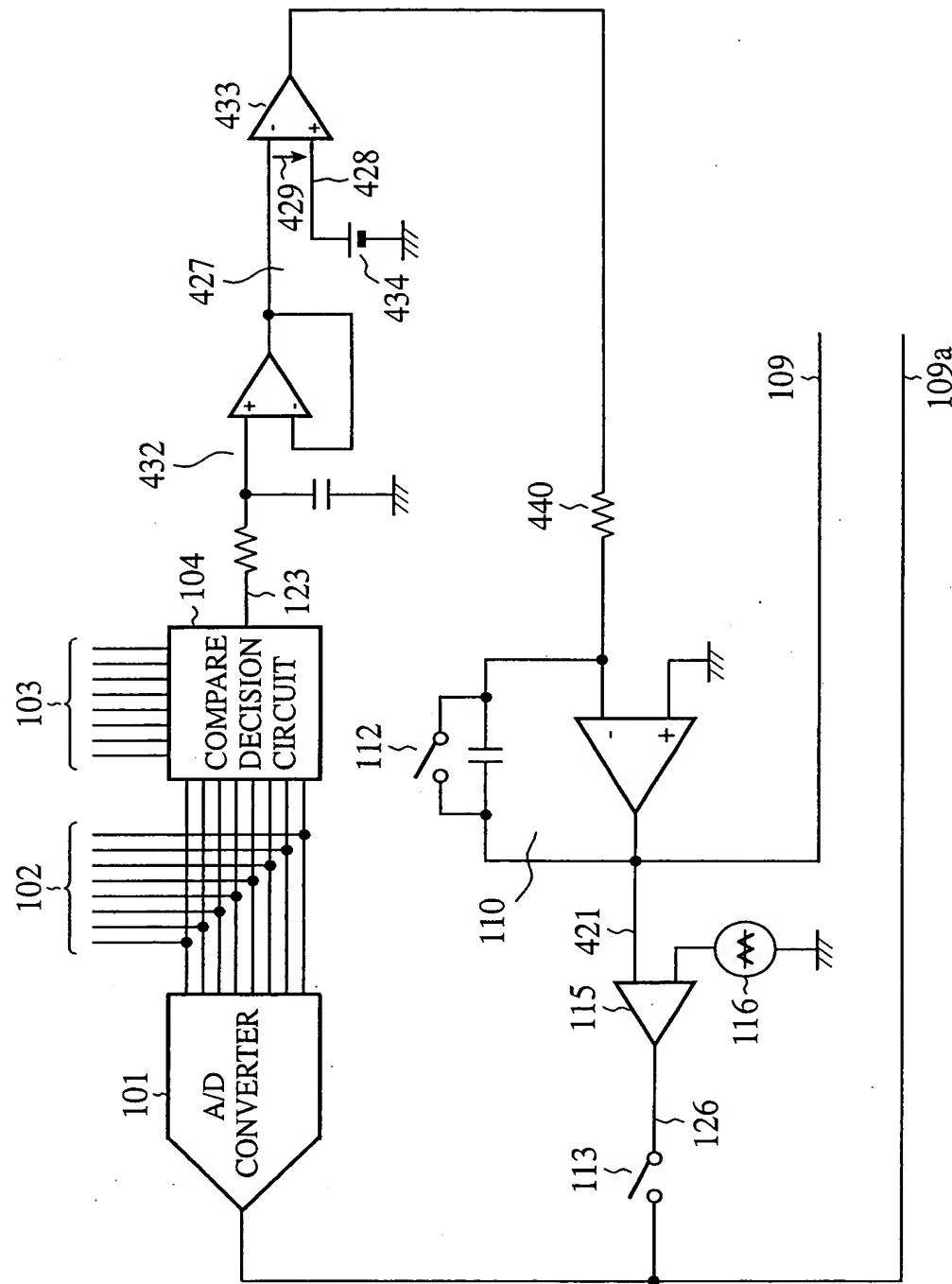


FIG.7

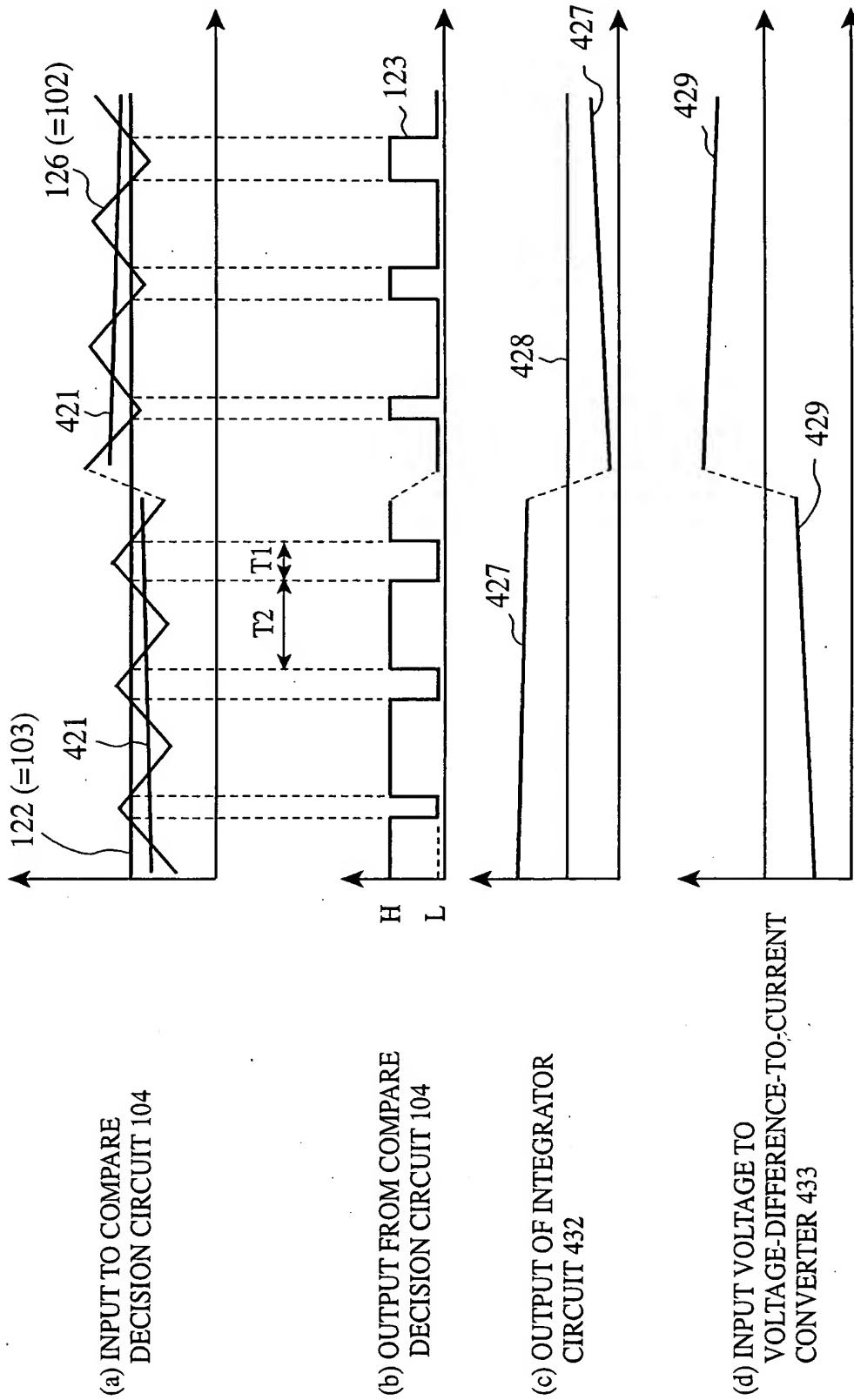


FIG.8 (PRIOR ART)

